

FIG. 1

FIG. 1 is a block diagram of a system 100 showing a first device 110 and a second device 120 connected by a communication link 130. The first device 110 includes a CPU 111, Memory 112, I/O 113, and a Comm. Adapter 114. The second device 120 includes a CPU 121, Memory 122, and a Comm. Adapter 123. The communication link 130 is connected to the Comm. Adapter 114 of the first device 110 and the Comm. Adapter 123 of the second device 120.

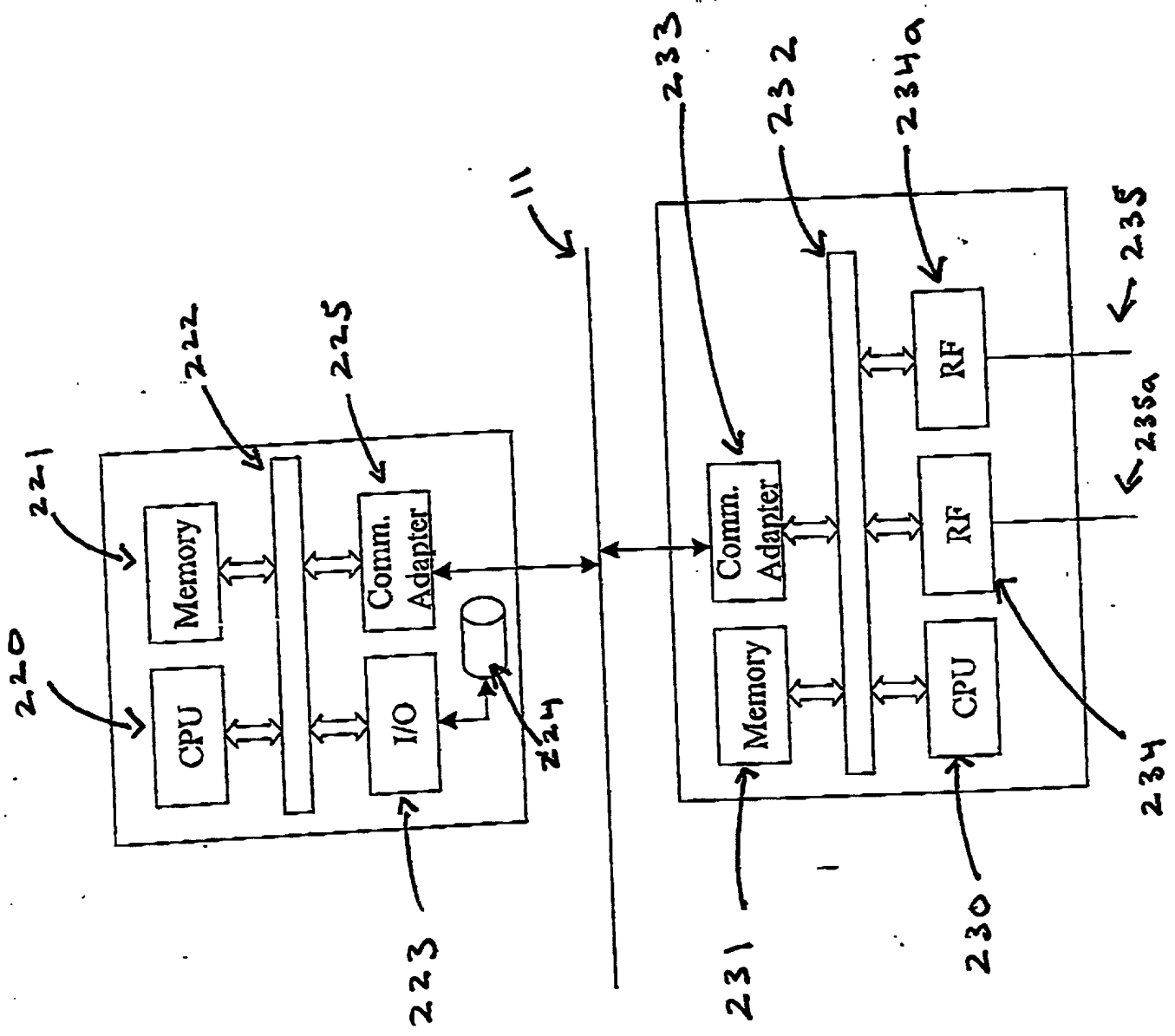


Fig 2

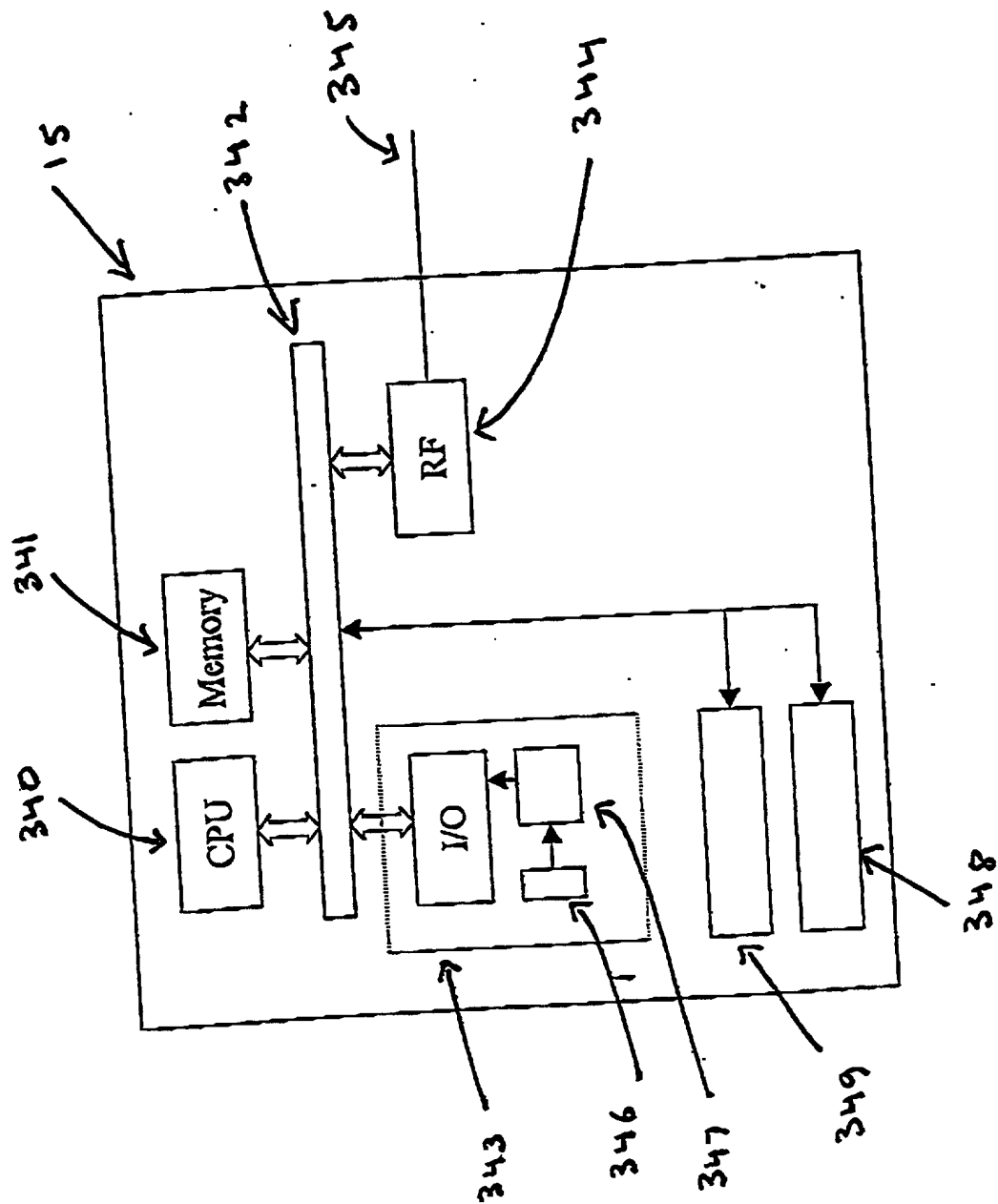


FIG. 3

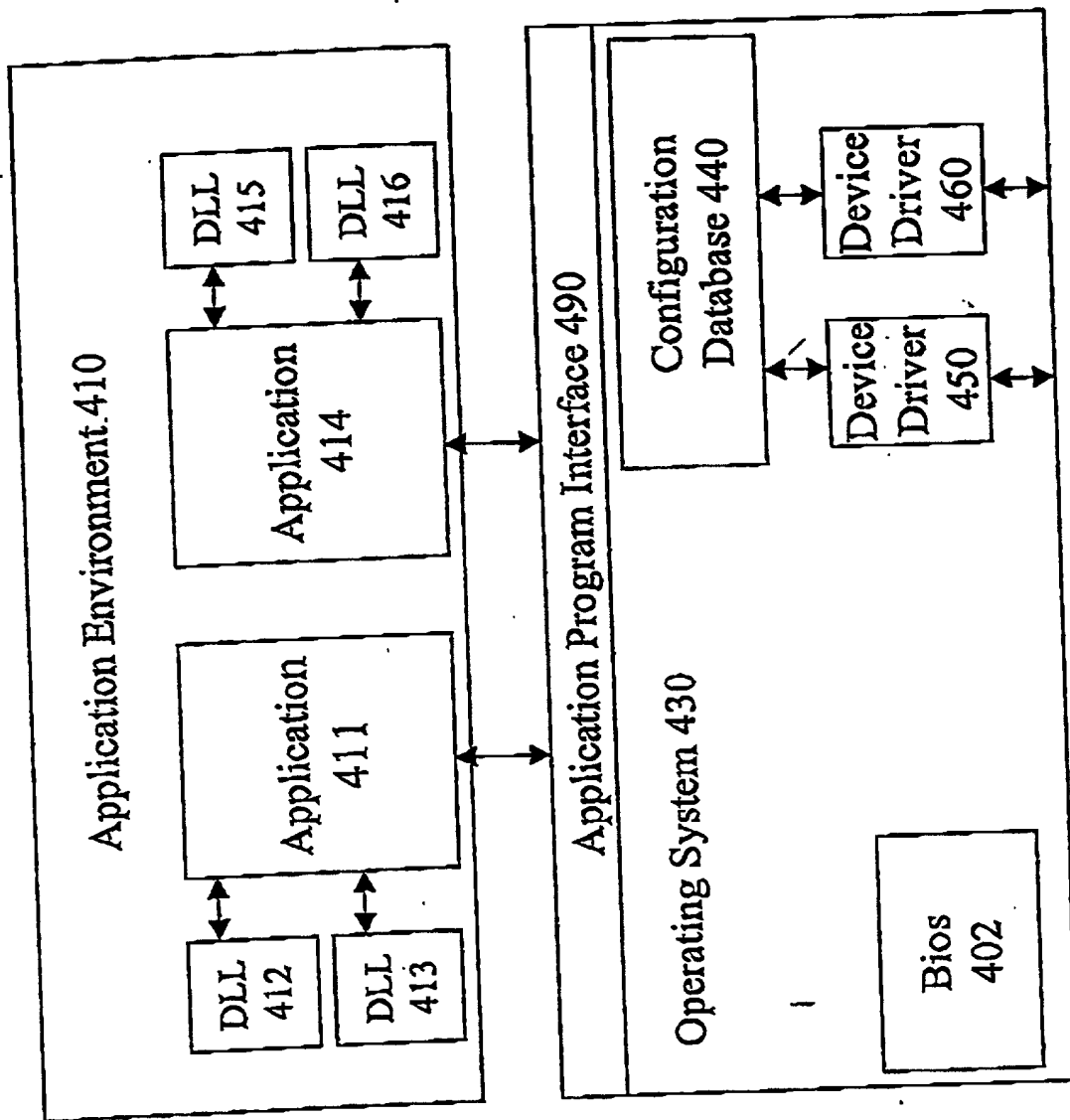


Fig. 4

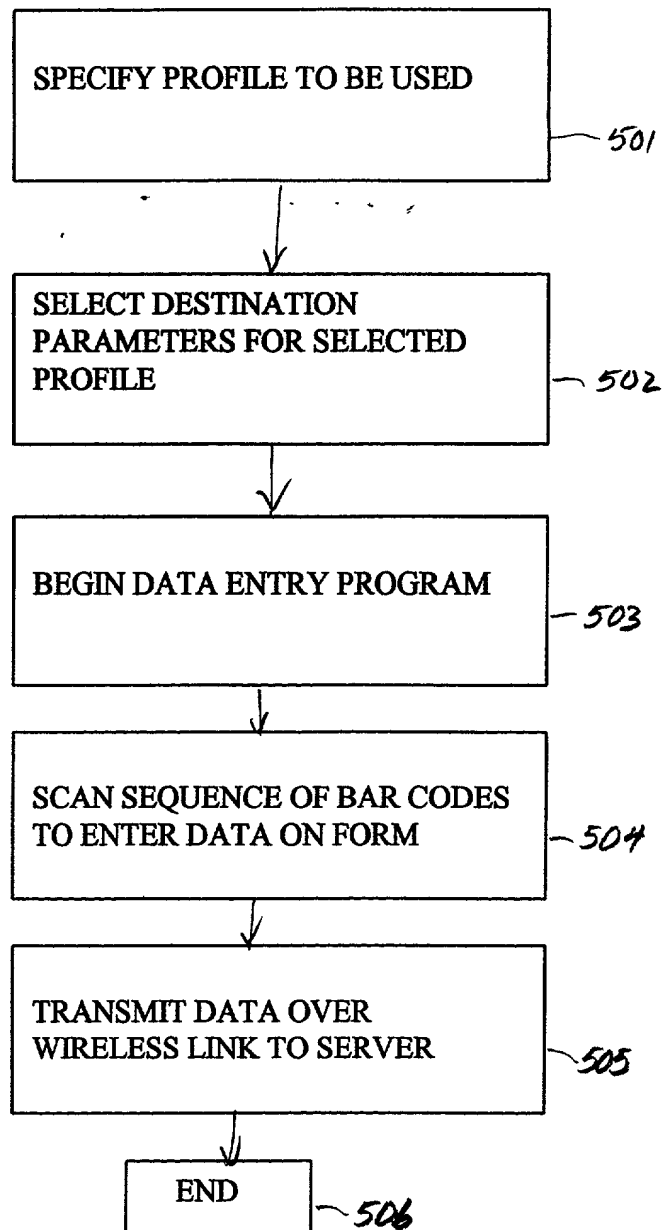


FIG 5

FIG 10

